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S/N 10/552301

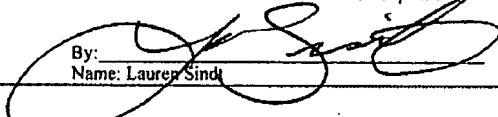
PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TSUKADA	Examiner:	BAISA, JOSELITO
Serial No.:	10/552301	Group Art Unit:	SASIS 2832
Filed:	October 7, 2005	Docket No.:	10921.0359USWO
Title:	CHIP RESISTOR AND METHOD FOR MANUFACTURING SAME		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on September
06 2007.

By:
Name: Lauren Sindt


PETITION FOR EXTENSION OF TIME

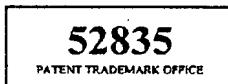
Mail Stop: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Commissioner:

In accordance with the provisions of 37 C.F.R. §1.136(a), it is respectfully requested that a 2-month extension of time be granted in which to respond to the outstanding Office Action mailed April 10, 2007, said period of response being extended from July 10, 2007 to September 10, 2007.

Please charge Deposit Account No. 50-3478 in the amount of \$450 to cover the required extension fee for a large entity.

Respectfully submitted,



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Dated: September 6, 2007

By: Curtis B. Hamre 09/06/2007 14:43 612.455.3801 10921.0359USWO 10552301
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CBH/ahk